| Notice of Allowability  | Application No.   | cation No. Applicant(s)   |                              |
|---|---|---|------------------------------|
|   | 10/041,896  | KRAUS ET AL.  |                              |
|   | Examiner  | Art Unit  |                              |
|   | Jennifer M. Dolan   | 2813  |                              |
| The MAILING DATE of this communication appe<br>All claims being allowable, PROSECUTION ON THE MERITS IS<br>herewith (or previously mailed), a Notice of Allowance (PTOL-85)<br>NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI<br>of the Office or upon petition by the applicant. See 37 CFR 1.313  | (OR REMAINS) CLOSED in<br>or other appropriate commu-<br>GHTS. This application is so | this application. If not include<br>nication will be mailed in due  | ded<br>e course. <b>THIS</b> |
| 1. 🖾 This communication is responsive to Amdt. and Remarks of   | of 3/31/04.   |   |                              |
| 2.  The allowed claim(s) is/are 22,23,25,28,65,67,68,70,71 and  | d 74-77.  |   |                              |
| 3. $\boxtimes$ The drawings filed on <u>07 January 2002</u> are accepted by the   | e Examiner.   |   |                              |
| <ul> <li>4.  Acknowledgment is made of a claim for foreign priority una a)  All b)  Some* c)  None of the:</li> <li>1.  Certified copies of the priority documents have</li> <li>2.  Certified copies of the priority documents have</li> </ul>   | been received.  | ,,  |                              |
| 3. Copies of the certified copies of the priority documents of the pr | • •   |   | ation from the               |
| Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.   |   | a reply complying with the re                                       | equirements                  |
| 5. A SUBSTITUTE OATH OR DECLARATION must be subm<br>INFORMAL PATENT APPLICATION (PTO-152) which give  |   |   | NOTICE OF                    |
| <ol> <li>CORRECTED DRAWINGS (as "replacement sheets") must</li> <li>(a)  including changes required by the Notice of Draftspers</li> <li>1)  hereto or 2)  to Paper No./Mail Date</li> <li>(b)  including changes required by the attached Examiner's Paper No./Mail Date</li> </ol>  | on's Patent Drawing Review .  |   | ,                            |
| Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t  | .84(c)) should be written on th<br>he header according to 37 CFI                      | e drawings in the front (not th<br>R 1.121(d).                      | ie back) of                  |
| 7. DEPOSIT OF and/or INFORMATION about the depo-<br>attached Examiner's comment regarding REQUIREMENT   |   |   | Note the                     |
| Attachment(s)   | 5 <b></b>   |   |                              |
| <ol> <li>Notice of References Cited (PTO-892)</li> <li>Dotice of Draftperson's Patent Drawing Review (PTO-948)</li> </ol>   | 6. 🔲 Interview Su   | ormal Patent Application (PTmmary (PTO-413),                        | ∤O-152)                      |
| 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 1/7/02,12/30/02, 11/20/03, 8/27/03, 5/2 4. ☐ Examiner's Comment Regarding Requirement for Deposit  | 18), 7. ☐ Examiner's /<br>9/oʒ  | Mail Date Amendment/Comment  Statement of Reasons for All           | lowance                      |
| of Biological Material  |   | CAPL WHITEHEAD, JR. PERVISORY PATENT EXAMINE TECHNOLOGY CENTER 2800 | adf                          |

## **DETAILED ACTION**

This action is in response to the Amendment filed 3/31/04

## Allowable Subject Matter

- 1. Claims 22, 23, 25, 28, 65, 67, 68, 70, 71, and 74-77 are allowed.
- 2. The following is an examiner's statement of reasons for allowance: The primary reason for allowance is that the prior art essentially fails to suggest DRAM storage capacitors having a dielectric region either comprising amorphous aluminum nitride of less than 60 angstroms thickness (cl 28), comprising aluminum nitride where the capacitor dielectric has a thickness of less than 50 angstroms(cl 25), or consisting essentially of aluminum nitride and a native oxide having a total thickness of less than 60 angstroms (cl 23).

The closest prior art of record is U.S. Patent No. 6,218,293 to Kraus et al. It is noted that the Kraus patent and the present application are both assigned to Micron technology, as per the applicant's statement in the Remarks filed on 3/31/04, and thus the '293 patent does not qualify as valid prior art for 35 U.S.C. 103 (a) rejections under 35 U.S.C. 102 (e). Kraus teaches a similar structure to that claimed in the present application, except that the aluminum nitride capacitive layer is not substantially amorphous, does not have a thickness of less than 50 angstroms, and does not include a native oxide layer, thus making the '293 reference inapplicable for 102 (e) based rejections.

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Aside from the Kraus reference supra, the closest prior art includes U.S. Patent No. 6,285,050 to Emma et al., which teaches a memory circuit including a decoupling capacitor having an optional aluminum nitride layer in the capacitor dielectric region. Since the capacitor in Emma is a decoupling capacitor, and since Emma teaches that the AlN layer is purely an optional layer accompanying a BST or high dielectric constant layer, and since Emma further fails to suggest a thickness for the capacitive layers, it is the examiner's opinion that it is well beyond the purview of a person having ordinary skill in the art to pluck the AlN layer out of the capacitive structure in Emma, insert it into a memory storage capacitor for a DRAM, and then select a very small thickness for such a layer.

U.S. Patent No. 5,977,582 to Fleming et al teaches a thin capacitor structure wherein the dielectric layer is formed of a dielectric TaAlON. It is the examiner's opinion, however, that a person skilled in the art would not consider the TaAlON of Fleming to be the same as or to be suggestive of the use of "aluminum nitride," due to the vastly different structure and properties between the two compositions. Thus, a person skilled in the art would not find it an obvious step to use AlN in place of the TaAlON, particularly when the disclosed composition of Fleming contains a very small portion of aluminum and nitrogen relative to the amount of tantalum and oxygen.

Since the valid prior art does not provide any reasonable suggestion of the usage of aluminum nitride in the capacitor dielectric layer of a DRAM memory cell, it is the examiner's opinion that a person skilled in the art would have been led to use conventional DRAM capacitor dielectric layers, rather than aluminum nitride.

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Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jennifer M. Dolan whose telephone number is (571) 272-1690. The examiner can normally be reached on Monday-Friday 8:30am-5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Carl W. Whitehead, Jr. can be reached on (571) 272-1702. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jennifer M. Dolan Examiner Art Unit 2813

jmd